Third harmonic injection by MMC-swiss rectifier for offshore HVDC wind turbine applications

Ali Salam Al-Khayyat¹, Mustafa Jameel Hameed¹, Alyaa Muhsen Manati¹

^{1,2,3}Department of Electrical and Electronics Engineering, University of Thi-Qar, Al-Nassiryah , Thi-Qar, Iraq

|--|

ABSTRACT

Received Jan 14, 2019

Keyword:

Modular Multilevel Converter, Swiss rectifier Third harmonic injection Voltage Source Converter HVDC. In this paper, Modular Multilevel Converter MMC-Swiss rectifier for harmonic mitigation is presented and simulated by PLECS software package. The mathematical calculations and system simulation are performed for a converter with nominal power of 400 MW and 400KV DC. The characteristic of Swiss rectifier including the operation principle, modulation strategy and relevant equations are described in detail. The MMC is used as a combination with Swiss rectifier because of its simplicity to be integrated for applications that required different level of voltages and currents, such as offshore wind turbine. Where, there is a trend to generate the electricity and transmit it in DC form due to its economic reasons. Therefore, the usage of MMC-Swiss rectifier is viable for High Voltage Direct Current HVDC applications. The contribution of Swiss rectifier is to make the current drawn from the supply by a converter, pure sinusoidal and this achieved by injecting third harmonic current in the circulating current from the DC side. Thus, this would reduce the THD and improve the power quality of the AC side currents.

Corresponding Author:

Ali Salam Al-Khayyat¹ Departement of Electrical and Electronics Engineering, College of Engineering, University of Thi-Qar, Al-Nassiryah , Thi-Qar, Iraq E-mail: ali-al-khayyat@utq.edu.iq

1. Introduction

The development of Power Electronic industry has increased the usage of non-linear loads. The harmonic in line current is an issue and this is caused by non-linear loads, which are considered as source of harmonic current. Finding the solution for reducing harmonic distortion is an issue in research and engineering field.

For three-phase Power Factor Correction (PFC) system, different topologies have been proposed for mitigating harmonics and some are based on third-harmonic current injection technique. Applications such as HVDC, electric vehicle battery charger, DC drive and future electric aircraft inherently require a converter from AC to DC [1,2]. One of the converter topologies, which are based on third-harmonic current injection, is Swiss rectifier. This rectifier would reduce the harmonics of input line current and under operating condition; it provides high efficiency [3]. In addition, it is not only used for harmonic improvement, but also it is fully controllable.

Swiss rectifier is introduced for first time in [4] and [5]. The structure of Swiss rectifier consists of three-phase diode rectifier with active switches placed in the main path of the current to/from the load, which works as third-harmonic current injection circuit.

HVDC offshore wind turbine has drawn the attention recently. Thus, this converter is appropriate for this purpose. The Modular Multilevel Converter MMC based on Voltage Source Converter VSC is suitable for HVDC applications. The MMC-VSC would improve the safety of electric power supply, where VSC would



provide decouple control for active and reactive power and back up the AC grid voltage. The MMC would be used in a combination with the Swiss rectifier. Where, a controlled DC voltage source as MMC branch will be connected to Swiss rectifier.

In this paper, the MMC-Swiss rectifier for offshore HVDC wind turbine applications would be implemented and controlled. The operation principle of this converter, their modulation and the relevant equations to get the rectifier parameters would be explained in details. PLECS® simulation package has been used in this paper due to its powerful tool in power electronic components and their control.

The outline of this paper would be as follows, after an introduction, Swiss rectifier and the related equations of voltage and current would be presented. Then, the modulation of Swiss rectifier switches is explained. After that, the modulation and Simulink result would be introduced. Then, mathematical calculations for voltage and current are explained. The results from the simulation and calculation are compared and discussed.

In order to understand the Swiss rectifier, the basic knowledge of three-phase diode rectifier is required. The source of diode rectifier is assumed ideal and AC side inductance is small to the extent that can be neglected. The PLECS software is used to obtain the result, which is based on simple calculation for the three-phase diode rectifier equations, which is shown in Figure 1.



Figure 1: Three-phase Diode Bridge

The ideal three-phase source are represented by:

$$\begin{cases} V_a = V_m \cos(wt) \\ V_b = V_m \cos(wt - 2\pi/3) \\ V_c = V_m \cos(wt - 4\pi/3) \end{cases}$$
(1)

The output current (I_{out}) is assumed to be positive and this means that the load is sensitive to the rectifier output. The diode behaviour of the three-phase diode rectifier is shown in Table 1, which represents the rectifier diode state functions [1,6,7].

Table 1. Diode state functions						
Segment	D1	D2	D3	D4	D5	D6
0 <wt<60°< td=""><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td></wt<60°<>	1	0	0	0	0	1
60° <wt<120°< td=""><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td></wt<120°<>	0	0	1	0	0	1
120° <wt<180°< td=""><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td></wt<180°<>	0	1	1	0	0	0
180° <wt<240°< td=""><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td></wt<240°<>	0	1	0	0	1	0
240° <wt<300°< td=""><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td></wt<300°<>	0	0	0	1	1	0
300° <wt<360°< td=""><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td></wt<360°<>	1	0	0	1	0	0

Table 1: Diode state functions

The rectifier can produce positive and negative output voltage, which is equal to the maximum and minimum phase voltage respectively, as expressed in Equations (2) and (3):

$$V_A = max(v_a, v_b, v_c) \tag{2}$$

$$V_B = \min(v_a, v_b, v_c) \tag{3}$$

The Fourier series of the positive and negative output voltages and the mean output voltage of the three-phase rectifier are given by the following Equations:

$$V_A = \frac{3\sqrt{3}}{\pi} V_m \left[\frac{1}{2} + \sum_{n=1}^{\infty} \frac{(-1)^{n+1}}{9n^2 - 1} \cos(3nwt)\right]$$
(4)

$$V_B = \frac{3\sqrt{3}}{\pi} V_m \left[-\frac{1}{2} + \sum_{n=1}^{\infty} \frac{1}{9n^2 - 1} \cos(3nwt) \right]$$
(5)

$$V_{Out} = V_A - V_B = \frac{3\sqrt{3}}{\pi} V_m = 1.66 V_m \tag{6}$$

The spectral components at odd triples are cancelled from the output voltage, because V_A and V_B have the same spectral. Therefore, the output voltage would be composed from only the 6th multiples of the fundamental frequency. The instantaneous output voltage can be expressed by:

$$V_o(t) = 0.9549 V_m (1 - \sum_{n=6k}^{\infty} \frac{2}{(n^2 - 1)} \cos \frac{n\pi}{6} \cos nwt)$$
(7)

The current shape of the three-phase diode rectifier is not sinusoidal due to harmonics; this is shown in Figure 2. It is required to obtain sinusoidal current for power electronic applications and this would be achieved by employing Swiss rectifier.



Figure 2: Phase current waveform in three-phase diode rectifier (Time vs. ampere)

The usage of MMC-Voltage Source Converter VSC in the HVDC transmission would add some advantage points, such as the possibility of obtaining independent control for the active and reactive power, and the AC voltage grid can be supported [8-16]. The MMC can be structured from identical sub-modules or cells, which are series connected. That combination would lead to high efficiency, easy scale up, lower device stress, easy manufacturability and reduction in harmonics. Half-bridge or full-bridge cell in each leg can be used in the

MMC, however, a cell with two-level half-bridge that contain two IGBTs connected in series across a capacitor is popular form in the converter, as shown in Figure 3.



Figure 3. Half-bridge sub-module (SM)

2. MMC voltage and current description

The MMC can be controlled via the switching strategy. It would be assumed that the number of active submodule for each phase is equal to n. Thus, the number of active sub-module in upper MMC leg is given by the following relationship:

$$m_{up,i} = n - m_{low,i} \tag{8}$$

Where $(m_{up,i})$ and $(m_{low,i})$ are the numbers of active sub-module in the upper and lower part of the leg respectively and (i = a, b and c). The capacitor DC voltage and current flow direction would be an issue to be considered in selecting which sub-modules are active or not [17]. When the average value of the DC bus is V_{DC} and this value is the same for the capacitors of active sub-modules, then the voltage of capacitor sub-module is given as:

$$V_{cap} = V_{DC}/n \tag{9}$$

Thus, from the switching state of the active sub-module, where each individual sub-module has two different terminal voltage values, 0 and V_{cap} . So, a series expansions of the terminal output voltage of each individual sub-module would represent the upper and lower voltages, as follows:

$$V_{up,i} = \sum_{i=1}^{n} (S_{up,i}) \left(V_{cap,up,i} \right) + L_{arm} \frac{di_{up,i}}{dt}$$
(10)

$$V_{low,i} = \sum_{i=1}^{n} (S_{low,i}) \left(V_{cap,low,i} \right) + L_{arm} \frac{di_{low,i}}{dt}$$
(11)

Where the switching states of upper and lower legs are denoted by $S_{up,i}$ and $S_{low,i}$ respectively. If $S_{up,i}$ or $S_{low,i}$ is equal to 1, the sub-module would be active otherwise it is inactive [17]. The converter phase current contains two components, which are the DC and AC current component. The following equations represent the current, which has two currents, upper and lower legs for each phase. The relationship between the upper and lower arm currents, and, the circulating current between the MMC phases (i = a, b and c) is shown in following Equations:

$$i_i = i_{up,i} + i_{low,i} \tag{12}$$

$$i_{up,i} = \frac{i_i}{2} + i_{circ,i} \tag{13}$$

$$i_{low,i} = -\frac{i_i}{2} + i_{circ,i} \tag{14}$$

Therefore:

$$i_{up,i} = i_{up,ac,i} + i_{circ,i} \tag{15}$$

$$i_{low,i} = i_{low,ac,i} + i_{circ,i} \tag{16}$$

Where $i_{circ,a}$, $i_{circ,b}$ and $i_{circ,c}$ are the currents flowing and they are depending only on the DC current value. The circulating current equation is given by:

$$i_{circ,i} = \frac{i_{dc}}{3} \tag{17}$$

Hence, the upper and lower arm currents can be expressed as AC and DC components:

$$i_{up,i} = i_{up,ac,i} + \frac{i_{dc}}{3} \tag{18}$$

$$i_{low,i} = i_{low,ac,i} + \frac{i_{dc}}{3} \tag{19}$$

3. Swiss rectifier

The harmonics are an issue in the power electronics application, but the development of the power electronic converter that would provide sinusoidal current and inject third-harmonic current has increased the usage of non-linear loads. One of the rectifier topology which achieves that is Swiss rectifier as shown in Figure 4. It provides high rectification efficiency, harmonics reduction in the AC side, and it is fully controllable. The general layout of Swiss converter is that it consists three-phase diode rectifier and some active switches in the main path of the input current, which their function is to inject third-harmonics current based on their switching operation [1,7]. The converter provides currents through both positive and negative switches, i_{T+} and i_{T-} . These currents are formed in proportion to two phase voltages that would create the diode bridge output voltages. Then, they are fed back to the mains through the configuration of a current injection, where the sinusoidal input current space vector would be used for the analysis of switches conduction states. The phase currents i_a , i_b and i_c and the rectifier input currents i_{ra} , i_{rb} and i_{rc} are assumed to be equal. In addition, the supply voltages are assumed ideal and sinusoidal.



Figure 4. Circuit topology of Swiss Rectifier

4. Swiss rectifier modulation

The Clark transformation is used to transform from the three-phase stationary system into two-phase stationary system known as $\alpha\beta$ stationary frame, as shown in Figure 5.



Figure 5. The three-phase and two-phase stationary reference frame of voltage

The mathematical Clark transformation is explained in the following matrix:

$$\begin{pmatrix} v_{\alpha} \\ v_{\beta} \end{pmatrix} = \frac{2}{3} V_m \begin{pmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{pmatrix} \begin{pmatrix} V_{(t)}^{a} \\ V_{(t)}^{b} \\ V_{(t)}^{c} \\ V_{(t)}^{c} \end{pmatrix}$$
(20)

The above equation can be written in another form, which is:

$$\begin{pmatrix} v_{\alpha} \\ v_{\beta} \end{pmatrix} = V_m \begin{pmatrix} \cos(wt) \\ \sin(wt) \end{pmatrix}$$
 (21)

Where the phase angle φ is defined as:

$$\varphi = \tan^{-1}(\frac{v_{\beta}}{v_{\alpha}}) \tag{22}$$

This angle is changing between 0° and 360° , thus it would be divided into six sectors. These six sectors have a length pointing to a vertices of a hexagon and the length of the active space vector is apart from each other's by 60° . Each section of that hexagon is called sector [18]. This is shown in Figure 6.



Figure 6. Hexagon of six-space vector of phase angle φ [1]

The modulation is achieved via the switches S_{y1} , S_{y2} and S_{y3} as they are shown in Figure 4, that have six sector intervals [0°, 60°]. Based on that, the sub-switch ($S_{y1,ab}$, $S_{y2,ab}$ and $S_{y3,ab}$) would be analysed for the interval [0, 30°], which means twelve 30° sectors, e.g., $0 < \varphi < 30°$, would make the main period. Four different state conductions for the main interval [0, 30°] would be achieved and they are described by T_+ and T_- . It should be mentioned that, this would occur in the half-wide sectors of the main periods. Figure 7 shows the four conduction states for [0, 30°] of the mains interval, where a combination of the two fast sub-switches would be on and this is described by the switching state of j=(ON, ON) and this indicates $j=(T_+, T_-)$. For instance, when j=(ON, ON), the rectifier input currents are $i_{ra} = I_{DC}$, $i_{rb} = 0$ and $i_{rc} = -I_{DC}$ [2][4]. Therefore, the injected current would be expressed as:



Figure 7. (a)-(d) Conduction States of the Swiss rectifier [2]

The order of first, third and fifth sector intervals [30°] are associated to the switches S_{y2} , S_{y1} , and S_{y3} respectively. Then, the input current space vector would be formed if the vector modulation that has the switching sequence of (ON,ON)-(ON,OFF)-(OFF,OFF)-(ON-OFF)-(ON,ON). The modulation of the Swiss rectifier injection current related to the switches S_{y1} , S_{y2} and S_{y3} is explain in Table (2).

Sector	S _{y1a}	S _{y1b}	S _{y2a}	S _{y2b}	S _{y3a}	S _{y3b}
$0^0 - 30^0$	0	0	1	1	0	0
$30^0 - 60^0$	0	0	1	1	0	0
$60^0 - 90^0$	1	1	0	0	0	0
$90^0 - 120^0$	1	1	0	0	0	0
$120^0 - 150^0$	0	0	0	0	1	1
$150^0 - 180^0$	0	0	0	0	1	1

$180^0 - 210^0$	0	0	1	1	0	0
$210^0 - 240^0$	0	0	1	1	0	0
$240^0 - 270^0$	1	1	0	0	0	0
$270^0 - 300^0$	1	1	0	0	0	0
$300^0 - 330^0$	0	0	0	0	1	1
$330^0 - 360^0$	0	0	0	0	1	1

The gate pulses for the third-harmonic injected current by this rectifier can produce with these switching state sequences. Hence, when the third harmonic current injection is controlled, therefore a current with sinusoidal shape in the supply can be obtained [1,2,28].

5. PLECS modelling results

The following results are based on PLECS simulation for the system shown in Appendix. The generated thirdharmonic currents is shown Figure 8. The phase current of the diode rectifier i_a is of a quasi-square wave shape, and its amplitude is varying in opposite phase to the six-pulse output voltage of the rectifier. Figures 9 and 10 are the PLECS simulation result of the rectifier without and with third-harmonic current being applied







Figure 9. Simulation result of scaled supply current without current injection circuit





6. Mathematical proof for sinusoidal shape input current

For the Swiss rectifier shown in Figure 11, it has assumed that the switches T_+ and T_- are switching with duty cycle K_1 and K_2 respectively.



Figure 11. Equivalent circuit of Swiss rectifier [1]

The rectifier would be considered as three-phase symmetrical load with phase conductance G [1]. So, based on that, the value of the injected current in phase b is given by the following equation:

$$i_y = -i_b = -GV_b \tag{24}$$

According to that, the voltage leg of the rectifier is given by:

$$V_{y} = V_{b} \tag{25}$$

The value of K_2 is equal to $(1-K_1)$, so this would result:

$$V_y = K_1 V_a + K_2 V_c = K_1 V_a + (1 - K_1) V_c = K_1 V_{ac} + V_c$$
(26)

Form the rectifier circuit shown in Figure 11, the duty cycle K_1 is expressed as:

$$V_{pos} = max(V_a, V_b, V_c) \tag{27}$$

$$V_{neg} = min(V_a, V_b, V_c) \tag{28}$$

$$K_{1} = \frac{-(V_{pos} + 2V_{neg})}{V_{pos} - V_{neg}} = \frac{V_{bc}}{V_{ac}}$$
(29)

Thus, the current flow through the switch T_+ is described as:

$$i_{T+} = K_1 i_y = -K_1 i_b = -K_1 G V_b = -G V_b \frac{V_{bc}}{V_{ac}}$$
(30)

Since the power is constant, therefore, the output current is given by:

$$\bar{I} = K_2 I_{out} = \frac{P}{V_{ac}} = \frac{i_a V_{ac} + i_b V_{bc}}{V_{ac}} = G \frac{V_a V_{ac} + V_b V_{bc}}{V_{ac}}$$
(31)

The current of phase a is proportional to the voltage of the main supply, where this voltage is sine wave in nature, therefore:

$$i_a = \bar{I} + i_{T+} = GV_a \tag{32}$$

By the same procedure, the current of phase c can be obtained and it is represented by the following equation:

$$i_c = GV_c \tag{33}$$

The formation of the current is as follows [12]:

$$i_a + i_b + i_c = 0$$
 (34)



Therefore, a current with sinusoidal shape for the mains interval $[0^\circ, 30^\circ]$ has been verified and the simulation waveform for three phase main current is shown in Figure 12.

Figure 12. Mains current $(i_{a,b,c})$ results of operation with current injection circuit by PLECS

The high rating and level of output voltage is an issue for the power electronic converters, where large stations and bulky transformer are required. In addition, higher number of voltage level would add extra cost and system complexity. The modular multilevel converter is solution for the mentioned issues, where high number of voltage level can be produced if an identical power electronic sub-modules are connected together in series. The modules can be easily added in order to obtain the redundancy and increase the reliability. The higher output voltage level, the more desirable output current and less need for the filtering. Thus, MMCs become the popular type for the Voltage Source Converter of HVDC. Therefore, a combination of Swiss rectifier and MMC would provide high voltage level and pure sinusoidal current in the input side; hence, this combination is suitable for HVDC application [11 - 15]. The simulation has conducted under the assumption of connecting the normal rectifier to a stiff AC grid and there is load on the DC side. The specifications of the rectifier are

listed in Table 3. In the Swiss rectifier, each leg is considered as a high voltage controlled switch, where large number of IGBTs connected in series. Thus, each sub-module can be considered as a controlled DC voltage source, and each leg is as individual controllable voltage source. Based on that, the converter is seen as a controllable voltage source instead of actual sub-modules with IGBTs [19,20,27]. Figure 13 is the MMC-Swiss rectifier, which is modelled in this paper.



Figure.13. Circuit topology of MMC-Swiss rectifier by PLECS Table 3: Rectifier specification

Items	Values
Input phase voltage, $\widehat{V}_{a,b,c}$	241.840 KV
Reference power, p	400 MW
Main resistor, R	400 Ω
MMC inductor, L	0.0001 H
MMC resistor, <i>R</i> _{<i>mmc</i>}	0.001Ω
Mean sub-module voltage, \overline{V}_{sm}	2KV
Sub-module capacitor, C_{sm}	4 mF

The waveform in Figure 14, is three-phase sinusoidal made up from the upper (F_+) , lower (F_-) and non-rectified (F_y) components. Based on the previous analysis and calculations, F_+ , F_- and F_y can be easily derived.

$$\begin{cases} x_a = \hat{x}\cos(wt) \\ x_b = \hat{x}\cos(wt - \frac{2\pi}{3}) \\ x_c = \hat{x}\cos(wt - \frac{4\pi}{3}) \end{cases}$$
(35)



By using Fourier series, the positive rectified component can be expressed as:

$$F_{+} = \frac{3\sqrt{3}}{\pi} \hat{\mathbf{x}} \left(\frac{1}{2} + \sum_{n=1}^{\infty} \frac{(-1)^{n+1}}{9n^2 - 1} \cos(3nwt) \right)$$
(36)

The negative component is:

$$F_{-} = \frac{3\sqrt{3}}{\pi} \hat{\mathbf{x}} \left(-\frac{1}{2} + \sum_{n=1}^{\infty} \frac{1}{9n^2 - 1} \cos(3nwt) \right)$$
(37)

From the point of view, the non-rectified component F_y , the blue waveform in the Figure 14, is a waveform of interest, which is obtained from the phase wave through the period when they are either at the maximum or minimum value [6,25,26]. However, the unrectified component can be easily calculated from the sum of the instantaneous values:

$$x_a + x_b + x_c = 0 \tag{38}$$

At every instant, one phase equals F_+ , another equals to F_- , and the remaining equals to F_y . Therefore, the addition of output waveform and the unrectified components would equal to zero [6,21,22]. Thus, the expression of the unrectified component is:

$$F_{y} = -F_{+} - F_{-} \tag{39}$$

$$F_{y} = -\frac{3\sqrt{3}}{\pi} \hat{\mathbf{x}} \sum_{n=1}^{\infty} \frac{2}{(6n-3)^{2}-1} \sin((6n-3)wt + \pi/2)$$
(40)

7. Analytical approach

Since the system is symmetrical, where the lower leg has the same result and calculation of the upper. Therefore, in this section, the calculations and analysis are based on the upper leg of the MMC, and the same can be obtained from the lower.

A. The injected current

The injected current can be easily obtained from the following:

$$i_y = \hat{1}F_y \tag{41}$$

Where it is a multiple of the peak phase current and non-rectified component. Then, the peak to peak injected current can be calculated as:

$$\hat{\mathbf{l}}_{y} = \pm \frac{3\sqrt{3}}{\pi} \hat{\mathbf{l}} \sum_{n=1}^{\infty} \frac{2}{(6n-3)^{2}-1} = \pm \frac{3\sqrt{3}}{\pi} \cdot \hat{\mathbf{l}} \cdot \frac{\pi}{6\sqrt{3}} = \pm \frac{1}{2} \hat{\mathbf{l}}$$
(42)

Where Î is the converter peak to peak current. The mean power is given by:

$$P = \frac{3}{2}\hat{V}\hat{I} \tag{43}$$

For the 400MW and 241.840kV system, the peak phase current is 1102 amp. The value of the peak diode current is 1100Amp, where the supply side inductances are small enough to be neglected.

$$\hat{\mathbf{I}}_{diode} = \frac{\hat{\mathbf{V}}\sqrt{3}}{R_{total}} \tag{44}$$

B. Voltage of upper leg

It has mention before, that the modulation of the switches for harmonic current injection has six main sectors intervals [0°, 60°] for each switch. The angle φ is changing between 0° and 360°. Therefore, the switches of MMC-Swiss rectifier are switched in an instant where they are in phase through the normal non-rectified periods. Figure 15 shows the relation between the positive rectified components (F_{y}) and non-rectified components (F_{y}). It is clearly observed that F_{y} and F_{+} have always $\pi/6$ phase difference (half period of one sector).



Figure.15. Relation between φ , F_+ and F_y

For the purpose of simplification, the Clarke transformation has been used. The positive component F_+ is a function of cosines and F_y would be as a sine function. Due to the periodic waveform of F_+ and F_y , the analytical calculations is performed through the interval between $-\pi/3$ and $\pi/3$. For a converter operation with current injection, the upper MMC voltage is:

$$V_{mmc}^{u} = \widehat{V}(F_{+} - F_{y}) \tag{45}$$

where \hat{V} is input phase voltage. Therefore, MMC upper can be expressed as:

$$V_{mmc}^{u} = \begin{cases} \widehat{\mathbb{V}}\left(\cos(\varphi + \frac{\pi}{3}) - \sin(\varphi + \frac{\pi}{6})\right) & -\pi/3 < \varphi < 0\\ \widehat{\mathbb{V}}\left(\cos(-\varphi + \frac{\pi}{3}) - \sin(-\varphi + \frac{\pi}{6})\right) & 0 < \varphi < \pi/3 \end{cases}$$
(46)

When the cosine or sine function is equal to one, $\varphi = -\pi/3$, the maximum and minimum value of the MMC upper voltage is obtained. Accordingly, the peak value of proposed rectifier voltage is 1.5 \hat{V} or 362.76kV.

C. Current injection of upper leg

In the MMC-Swiss rectifier, the current is divided into upper and lower leg, and the value of this current is half value of the injected current. Thus, the upper current of the MMC-Swiss rectifier is:

$$i_{mmc}^{u} = \begin{cases} \frac{1}{2}\sin(\varphi + \pi/6) & -\pi/3 < \varphi < 0\\ \frac{1}{2}\sin(-\varphi + \pi/6) & 0 < \varphi < \pi/3 \end{cases}$$
(47)

Therefore, the peak value of MMC-Swiss rectifier upper current is:

$$\hat{\mathbf{I}}_{mmc}^{u} = \frac{\hat{\mathbf{I}}}{4} \tag{48}$$

The peak value of the upper leg is 275.5Amp, which is obtained from the above formula, and the Root-Mean-Square (RMS) of the upper current of MMC-Swiss rectifier is 161.1 amp, which can be obtained from Equation (49).

$$i_{RMS}^{u} = \left[\frac{1}{T} \int_{0}^{T} f_{(t)}^{2} dt\right]^{1/2} = \frac{3\sqrt{3}}{4\pi} \frac{\hat{l}}{2\sqrt{2}}$$
(49)

The result for the lower leg of the converter is the same, but it should consider the negative sign for the phase current [21-24].

D. Circulating current

The average value of upper and lower leg currents would be named as the circulating current:

$$i_{circ} = \frac{i_{mmc}^u + i_{mmc}^l}{2} \tag{50}$$

The circulating current would have second harmonic component even if higher order harmonics are neglected [19, 20]. The circulating current could damage the sub-modules and main converter, thus, this current must be reduced to a value that would guarantee safe operation. No mean power exchange is desired for the converter operation. Hence, in order to achieve that, the mean power must be forced to zero value. That can be performed by circulating the current that is injected into both legs, where if i_{circ} is selecting such that:

$$i_{circ} = \frac{2\bar{p}_{MMC}^{u}}{V_{DC}}$$
(51)

The mean power Equation (43) and DC voltage values are obtained before, and from these values, the value of the circulating current of the MMC-Swiss rectifier is 86.45 amp. Moreover, the value of the upper and lower leg current would change when the i_{circ} is injected. Thus, the new current of the MMC is:

$$i_{mmc,new}^{u} = \begin{cases} \frac{1}{2}\sin(\varphi + \pi/6) + i_{circ} & -\pi/3 < \varphi < 0\\ \frac{1}{2}\sin(-\varphi + \pi/6) + i_{circ} & 0 < \varphi < \pi/3 \end{cases}$$
(52)
$$i_{mmc,new}^{l} = \begin{cases} -\frac{1}{2}\sin(\varphi + \pi/6) + i_{circ} & -\pi/3 < \varphi < 0\\ -\frac{1}{2}\sin(-\varphi + \pi/6) + i_{circ} & 0 < \varphi < \pi/3 \end{cases}$$
(53)

The peak and RMS values of new current are 361.95 and 183.5 amp respectively. The equivalent circuit of the MMC-Swiss rectifier is shown in Figure 16, and in this scheme, the MMC is represented by the voltage source.

The instantaneous power of the MMC is derived as:



$$p_{MMC}{}^{u} = V_{mmc}^{u} \cdot i_{MMC}{}^{u} \tag{54}$$

Figure 16. Equivalent schematic circuit of MMC-Swiss rectifier

By integrating the above equation, the mean power of 400MW, 241.840kV system can be obtaind:

$$\bar{p}_{MMC}{}^{u} = \left(\frac{3}{2\pi}\right) \left(\int_{-\frac{\pi}{3}}^{0} V_{mmc}^{u} \cdot i_{mmc}^{u} + \int_{0}^{\frac{\pi}{3}} V_{mmc}^{u} \cdot i_{mmc}^{u} \right)$$
(55)

$$\bar{p}_{MMC}^{\ u} = (\frac{3}{2\pi})(\frac{\widehat{V}\widehat{I}}{2})\left(\frac{1}{8}(3\sqrt{3}-2\pi) + \frac{1}{8}(3\sqrt{3}-2\pi)\right) = -17.29 \text{ MW}$$

Due to the small resistor value of each leg of the converter, the effect of V_{circ} of the MMC is neglected. Hence, the new mean value of the power for the upper cell is:

$$\bar{p}_{MMC}^{u,new} = \left(\frac{3}{2\pi}\right) \left(\int_{-\frac{\pi}{3}}^{0} V_{mmc}^{u} \cdot i_{mmc,new}^{u} + \int_{0}^{\frac{\pi}{3}} V_{mmc}^{u} \cdot i_{mmc,new}^{u} \right)$$
(56)

The result of Equation (56) is approximately zero, therefore no mean power exchange has proven , moreover safe and sustained operation has achieved.

8. Results and discussion

The modelled MMC-Swiss rectifier by PLECS is shown in Figure 17 and the proposed system with control is in the Appendix.



Figure 17. Proposed MMC-Swiss rectifier model in PLECS

The circulating current has injected in the upper and lower legs, and there would be circulating current and F_y in each leg of the converter. The following response is the current and voltage of upper and lower leg.



Figure 18: Responses of current and voltage in upper and lower arm

From the current and voltage response, it has observed that they are symmetrical and the lower current/voltage is opposite to the upper current/voltage. The peak values of the current and voltage were calculated as 361.95Amp and 362.76KV, respectively, and this shows the accuracy of the math calculations that has done before.

In Figure 19, it has observed that without circulating current being considered, the mean power of the rectifier is -17.29 MW, which is based on Eq.55. Furthermore, by injecting appropriate circulating current, low average power exchange could be achieved, and this is shown in Figure 20.

The power response of Figure 19 and 20 shows the validity between the obtained mathematical results and simulated result. According to the parameters of the rectifier in Table 3, the results obtained from the mentioned equations and PLECS simulation are compared. It has noticed that there is accuracy of the calculations with the simulation results, as shown in Table 4.



Figure 20. Power response with circulating current



Figure 19. Power response without circulating current

Parameters	Analytical Calculations	Simulation	Deviation (%)
Î (Amp)	1102	1096.96	-0.46
Î _{diode} (Amp)	1100	1096.96	0.3
$\hat{\mathbf{I}}^{u}_{mmc}(\mathbf{Amp})$	275.5	275.664	0.06
i ^u _{RMS} (Amp)	161.1	161.749	0.4
$\hat{\mathbf{I}}^{u}_{MMC,new}(\mathrm{Amp})$	361.95	362.1	0.04
<i>i^u_{RMS,new}</i> (Amp)	183.5	183.477	-0.012
W sm _{Total} (Joules)	8.06	8.069	0.1
ΔE^{max} (Joules)	1320	1212	-8
$\overline{p}_{MMC}^{u}(Watt)$	-17290336	-17301700	0.065
$\widehat{\mathbf{V}}_{mmc}^{u}(\mathbf{V})$	362760	362760	0
$V_{max}^{sm}(kV)$	2.074	2.048	-1.2
$V_{max}^{sm}(kV)$	1.926	1.950	1.5

From the obtained result, the converter performance has improved, and the current drawn by the converter is sinusoidal. Where the total harmonic distortion has reduced tremendously. The simulation and design process have been performed for a converter with nominal power of 400MW and 400KV DC voltage in order to certify the approach used in the calculation steps. Where, there is minor difference between the simulation and calculation results.

The results that have obtained from the mathematical analysis and PLECS simulation are matched. The MMC-Swiss rectifier has designed, the value of the leg inductance and resistance used in the simulation are small to the extent that can be neglected. The MMC-Swiss rectifier has been implemented on PLECS and various responses have been obtained. The controllable voltage source has considered as a MMC with 200 sub-module connected in series.

9. Conclusions

The VSCs considered as more suitable selection for HVDC applications compared to other types of converters. However, the modular multilevel converters (MMC) has proven its high efficiency and robustness as a converter. In addition, it contributes to reduce the HVDC stations because of its contribution of minimising the size of required filters as well as eliminating low order harmonics. The MMC-Swiss rectifier has employed in this paper to inject third-harmonic current to improve the quality of drawn AC current by the rectifier for HVDC applications. The simulation model was conducted by inserting three switches in the main current path, which acts as a circuit for third-harmonic current injection. The combination of MMC and Swiss rectifier ensure more flexible in terms of voltage and current sustainability, also the THD of the AC input current has improved without affecting the other equipment. The analytical calculations of the converter parameters have shown a good match with the obtained result from simulation with very low error.

References

- B. Soeiro, F.Vancu, J.W. Kolar, "Hybrid Active Third-Harmonic Current Injection Mains Interface Concept for DC Distribution Systems", Power Electronics, IEEE Transactions on Volume:28, Issue: 1, pp 7 – 13, Jan. 2013.
- [2] B. Soeiro, J.W. Kolar, T. Friedli, "Swiss rectifier A novel three-phase buck-type PFC topology for Electric Vehicle battery charging", Applied Power Electronics Conference and Exposition (APEC), 2012 Twenty-Seventh Annual IEEE, pp. 2617 – 2624, 5-9 Feb. 2012.
- [3] AD. Rajapakse, AM. Gole, PL. Wilson, "Electromagnetic transients simulation models for accurate representation of switching losses and thermal performance in power electronic systems", Power Delivery, IEEE Transactions on (Volume:20, Issue: 1), PP. 319 – 327, Jan 2005.
- [4] J. W. Kolar and T. Friedli, "The Essense of Three-Phase PFC Rectifier Systems", in Proc. 33rd IEEE Int. Telecom. Energ. Conf (INTELEC 2011), Oct. 9-13, pp. 1-27, 2011.
- [5] J. W. Kolar, M. Hartmann and T. Friedli, "Three-Phase Unity Power Factor Mains Interfaces of High Power EV Battery Charging Systems", in Power Electronics for Charging Electric Vehicles ECPE Workshop, Mar. 2011.
- [6] P. Pejovic (2007), "Three-Phase Diode Bridge Rectifier with Low Harmonics", PP. 7-21, Springer US press.
- [7] L. Xiaoqing, G. Chen, "Harmonic suppression technology of three-phase diode rectifier based on third harmonic current injection", Power Electronics and Applications, European Conference on Aalborg, PP. 1-9, 2-5 Sept. 2007.
- [8] S.M.Muyeen, R.Takahashi, T.Murata, and J.Tamura"Control strategy for HVDC interconnected DCbased offshore wind farm, " Electrical Machines and Systems, 2009. ICEMS 2009. International Conference.
- [9] R. Feldman, M. Tomasini, J. Clare, P. Wheeler, D. Trainer, and R. Whitehouse, "A Low Loss Modular Multilevel Voltage Source Converter for HVDC Power Transmission and Reactive Power Compensation," in IET–ACDC 2010, London, United Kingdom, Oct. 20–21, 2010.

- [10] J. Pan, R. Nuqui, K. Srivastava, T. Jonsson, P. Holmberg, and Y.-J. Hafner, "AC Grid with Embedded VSC-HVDC for Secure and Efficient Power Delivery," in Energy 2030 Conference, 2008. ENERGY 2008. IEEE, 17-18 2008, pp. 1 –6.
- [11] N. Flourentzou, V. Agelidis, and G. Demetriades, "VSC-Based HVDC Power Transmission Systems: An Overview," Power Electronics, IEEE Transactions on, vol. 24, no. 3, pp. 592 –602, March 2009.
- [12] Schettler, H. Huang, and N. Christl, "HVDC transmission systems using voltage sourced converters design and applications," in Power Engineering Society Summer Meeting, 2000. IEEE, vol. 2, 2000, pp. 715-720 vol. 2.
- [13] E.Spahic, G.Balzer "Offshore wind farms VSC-based HVDC connection," Power Tech, 2005 IEEE St. Petersburg, Russia.
- [14] F.Obeidat, X.Lie, L.Yongdong, "Simulation of grid connected HVDC offshore wind farm topologies," Power Electronics and Drive Systems (PEDS), 2013 IEEE 10th International Conference.
- [15] Michael Bahrman, et al. "Asynchronous back-to-back HVDC link with voltage source converters", at Minnesota Power Systems Conference, November 1999, USA
- [16] Vijay K. Sood. "HVDC and FACTS controllers: application of static converters in power systems" Boston ; London : Kluwer Academic Publishers, c2004.
- [17] F.T. Ghetti, A. Ferreira, H. Braga, P. Barbosa, "A study of shunt active power filter based on modular multilevel converter (MMC)", Industry Applications (INDUSCON), 2012 10th IEEE/IAS International Conference, PP. 1 – 6, 5-7 Nov. 2012, Fortaleza.
- [18] Ashish B Chaudhari, S. Jebarani Evangeline, "Space Vector Pulse Width Modulation Technique to Reduce the Effects of Voltage Unbalances", International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 – 8958, Volume-3, Issue-4, April 2014.
- [19] Amankwah, J. Clare, P. Wheeler, and A. Watson, "Cell capacitor voltage control in a parallel hybrid modular multilevel voltage source converter for HVDC applications," in IET-PEMD 2012, Bristol, United Kingdom, Mar. 27-29, 2012.
- [20] Maryam Saeedifard, Reza Iravani, "Dynamic Performance of a Modular Multilevel Back-to-Back HVDC System", IEEE Transactions on Power Delivery, vol. 25, no. 4, October 2010.
- [21] P. Bozovic, p. Pejovic, "Current-injection based low harmonic three-phase diode bridge rectifier operating in discontinuous conduction mode"Electric Power Applications, IEE Proceedings -(Volume:152, Issue: 2), 4 March 2005
- [22] P. Pejovic, "Two three-phase high power factor rectifiers that apply the third harmonic current injection and passive resistance emulation", Power Electronics, IEEE Transactions on Volume:15, Issue: 6, PP. 1228 – 1240, Aug. 2002.
- [23] B. Jacobson, P. Karlsson, G. Asplund, L. Harnefors and T. Jonsson, "VSC-HVDC Transmission with Cascaded Two-Level Converters", CIGRE 2010.
- [24] M.Sleiman, et al, "A survey on modeling, control, and dc-fault protection of modular multilevel converters for HVDC systems", Industrial Electronics (ISIE), 2014 IEEE 23rd International Symposium, PP. 2149 – 2154, 1-4 June 2014, Istanbul, Turkey.
- [25] Barazanchi, I. Al, Abdulshaheed, H.R., Shawkat, S.A., and Binti, S.R., 2019. Identification key scheme to enhance network performance in wireless body area network. Periodicals of Engineering and Natural Sciences, 7 (2), pp.895–906.
- [26] Rashid, S., Ahmed, A., Barazanchi, I. Al, and Jaaz, Z.A., 2019. Clustering algorithms subjected to Kmean and gaussian mixture model on multidimensional data set. Periodicals of Engineering and Natural Sciences, 7 (2), pp.448–457.
- [27] B. Durakovic, "Design of Experiments Application, Concepts, Examples: State of the Art," Periodicals of Engineering and Natural Scinces, vol. 5, no. 3, p. 421–439, 2017.

Appenidx A:



Figure A: Proposed MMC-Swiss rectifier model in PLECS